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Thder the Paperwork	Reduction Act of 1995, no pers	sons are required to res	pond to a collection of inform Application Number	nation unless it displays a valid OMB control number
TRANSMITTAL FORM			Filing Date	February 6, 2004
			First Named Inventor	Yutao MA
(to be us	ed for all correspondence after	rinitial filing)	Art Unit	Not Yet Assigned
,	•	3,	Examiner Name	Not Yet Assigned
Total Numbe	r of Pages in This Submiss	sion 6 + 7 refs.	Attorney Docket Numb	188122000700
	EN	ICLOSURES	(Check all that ap	oly)
Fee Transr	mittal Form	Drawing(s)		After Allowance Communication to Group
Fee /	Attached	Licensing-related Papers		Appeal Communication to Board of Appeals and Interferences
Amendment/Reply Petition				Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
After Final Petition to C				Proprietary Information
Affidavits/declaration(s) Power of Change of			rney, Revocation rrespondence Address	Status Letter
Extension of Time Request Termin			claimer	X Other Enclosure(s) (please identify below):
Express At	pandonment Request	Request for Refund		Form PTO-1449 (2 pageswith copy) Seven (7) Cited References
x Information pages)	Disclosure Statement (3	CD, Number of CD(s)		Return Receipt Postcard
Certified Co	opy of Priority			
Response	to Missing Parts/ Application	Remarks		
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unde	r 37 CFR 1.52 or 1.53			
	SIGNATU	JRE OF APPLICA	NT, ATTORNEY, OF	AGENT
Firm or Individual name	MORRISON & FOEI Thomas Chan - 51,5		(Customer No.	25226)
Signature	De			
Date	June 17, 2004			10 M M M M M M M M M M M M M M M M M M M

	spondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail, in IS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date
shown below.	
Dated: June 17, 2004	Signature:(Kerry C. Keehan)



Docket No. 188122000700

Client Reference: CAD:03-021

CERTIFICATE OF MAILING BY "FIRST CLASS MAIL"

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, PO Box 1450, Alexandria, VA 2231291450, on June 11, 2004.

Kerry C. Keehan

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Yutao MA et al.

Serial No.: 10/773,541

Filing Date: February 6, 2004

For: MODEL STAMPING MATRIX CHECK

TECHNIQUE IN CIRCUIT

SIMULATOR

Examiner: Not Yet Assigned

Group Art Unit: 2825

INFORMATION DISCLOSURE **STATEMENT UNDER 37 C.F.R. § 1.97 & 1.98**

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. § 1.97 and § 1.98, Applicants submit for consideration in the above-identified application the documents listed on the attached Form PTO-1449. Copies of the documents are also submitted herewith. The Examiner is requested to make these documents of record.

	I his li	nformation Disclosure Statement is submitted:			
	With t	he application; accordingly, no fee or separate requirements are required.			
	Before	Before the mailing of a first Office Action after the filing of a Request for Continued			
	Examination under § 1.114. However, if applicable, a certification under 37 C.F.R. §				
	1.97(e	e)(1) has been provided.			
\boxtimes	Within	n three months of the application filing date or before mailing of a first Office			
	Action on the merits; accordingly, no fee or separate requirements are required.				
	Howe	However, if applicable, a certification under 37 C.F.R. § 1.97(e)(1) has been provided.			
	After	receipt of a first Office Action on the merits but before mailing of a final Office			
	Action or Notice of Allowance.				
		A fee is required. A check in the amount of is enclosed.			
		A fee is required. Accordingly, a Fee Transmittal form (PTO/SB/17) is attached			
		to this submission in duplicate.			
		A Certification under 37 C.F.R. § 1.97(e) is provided above; accordingly; no fee			
		is believed to be due.			
	After mailing of a final Office Action or Notice of Allowance, but before payment of the				
	issue fee.				
		A Certification under 37 C.F.R. § 1.97(e) is provided above and a check in the			
		amount of is enclosed.			
		A Certification under 37 C.F.R. § 1.97(e) is provided above and a Fee Transmittal			
		form (PTO/SB/17 is attached to this submission in duplicate.)			

Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

The information contained in this Information Disclosure Statement under 37 C.F.R. § 1.97 and § 1.98 is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the above information constitutes prior art to the subject invention.

In the unlikely event that the transmittal form is separated from this document and the Patent Office determines that an extension and/or other relief (such as payment of a fee under 37 C.F.R. §1.17(p)) is required, Applicants petition for any required relief including extensions of time and authorize the Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to Deposit Account No. 03-1952 referencing 188122000700. However, the Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Dated: June 17, 2004

Respectfully submitted,

Thomas Chan

Registration No. 51,543

Morrison & Foerster LLP

755 Page Mill Road Palo Alto, California 94304-1018

Telephone: (650) 813-5616 Facsimile: (650) 494-0792

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Form PTO-1449			Docket Number 188122000700 Application Number 10/773,541				341		
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					Mailing Date June 1, 20	04			
			U.S. PAT	ΓΕΝ	T DOCUMENTS				
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Examiner Initials	Ref. No.	Title	101						******
	1.	Achar, R. et al., (May 2001). "Simulation of High-Speed Interconnects," <i>Proceeding of the IEEE</i> 89(5):693-728.							
	2.	Bomhof, C. W. (2001). "Introduction," Chapter 1 <i>In</i> Interative and Parallel Methods for Linear Systems, With Applications in Circuit Simulation, located at http://www.library.uu.nl/digiarchief/dip/diss/1957853/c1.pdf >, last visited on June 15, 2004, 15 pages (Includes Table of Contents).							
	3.	Devgan, A. (1995) "Efficient and Accurate Transient Simulation in Charge-Voltage Plane," International Conference on Computer Aided Design (ICCAD '95) Proceedings of the 1995 IEEE/ACM international conference on Computer-aided design, San Jose, California, United States, Pages: 110 - 114.							
	4.	International	V. et al., (1998). "Sim Conference on Compinternational conferer 8.	putei	r Aided Design (ICC	AD '98), P	roceedings of	the 1998	
	5.		E. et al., (2000). "Str Theor. Appl. 28:131-1		ral Analysis For Elec	tric Circui	ts and Consequ	uences for	·MNA,"
	6.	Krummenacher, F. et al., (July 28, 2000). "RF EKV MOSFET Model Implementation," EPFL - Electronics Laboratories (LEG), EPF-Lausanne, Switzerland, Craft European Project No. 25710, July 28, 2000, WP2, Deliverable D2.1, Ecole Polytechnique Federale de Lausanne 4 pages.							
	7.	Zhou, X. (2000). "Multi-Level Modeling of Deep-Submicron Mosfets and ULSI Circuits," 9th International Conference on Mixed Design of Integrated Circuits and Systems, Mixdes 2002,							
EXAMI	NER:		<u> </u>		DATE CONSI	DERED:			

EXAMINER: Initial if citation considered, whether or not the citation conforms with MPEP 609. Draw a line through the citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.

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PTO/SB/ 08 (2-92)

FORM PTO-1449
INFORMATION DISCLOSURE CITATION
IN AN APPLICATION
(Use several sheets if necessary)

Wroclaw, Poland, June 20-22, 2002, 7 pages .

PTO/SB/08 (2-92)
Sheet 2 of 2

Docket Number 188122000700

Applicant

Yutao MA et al.

Filing Date February 6, 2004

Group Art Unit 2825

Mailing Date June ____, 2004

EXAMINER:

DATE CONSIDERED:

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